

The World Leader in High Performance Signal Processing Solutions



Introduction to VisualDSP++ Tools

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This module will give the user an overview of the VisualDSP++ Tools. For demonstration purposes the ADSP-BF537 EZ-KIT Lite will be used as the target.

Users will see some quick tips on how to analyze their applications and fine tune them.



Module Outline

- Blackfin CrossCore Product offerings
- ADSP-BF537 EZ-KIT Lite configuration
- The VisualDSP++ Tools
 - Creating A Project
 - Plotting
- Performance improvement using chip features
 - Cache
 - L1 Memory
 - Voltage Regulator
- Creating an Ethernet Application



Blackfin CROSSCORE Product Line Free support, no maintenance fees

VisualDSP++ development suite

Performance-tuned C/C++ compiler
Source language debugger
RTOS kernel (VDK)
Device drivers and system services
Ethernet and USB 2.0 libraries
Tuned DSP libraries
Visual linker
Code wizards
Flash programmer
Multicore code generation and debugging
Plotting and other advanced code generation and debugging tools
Automation API

EZ-KIT Lite and Daughter cards

ADSP-BF533 ADSP-BF535 ADSP-BF537 ADSP-BF561

A-V EZ-Extender Audio EZ-Extender USB-LAN EZ-Extender FPGA EZ-Extender

JTAG Emulators

USB 2.0 USB 1.1 PCI

Background Telemetry



For Additional Information

- Getting Started with the ADSP-BF537 EZ-KIT Lite Manual
- Tools manuals provided in the On-line help
- Analog Devices web site: www.analog.com
- Email Support:
 - For tools questions: <u>Processor.tools.support@analog.com</u>
 - For processor questions: <u>Processor.support@analog.com</u>
 - Licensing and Registration questions:
 Processor.tools.registration@analog.com

Or click the "Ask A Question" button

